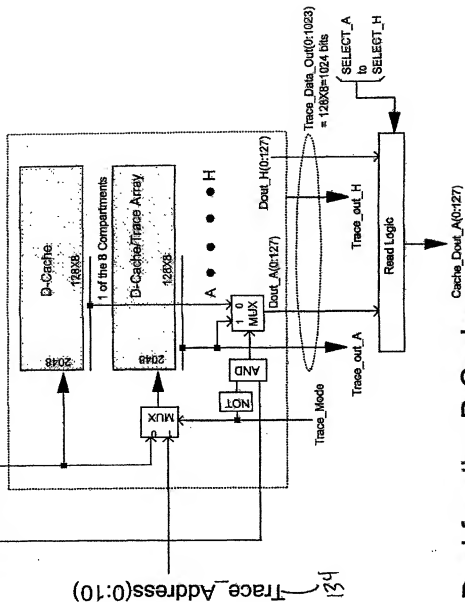


40

System Address
(A20) (A21:A31)

Trace_Address(0:10)

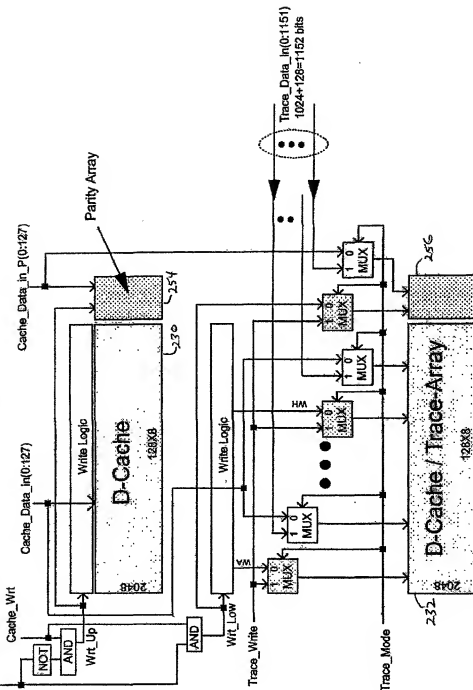


Dual function D-Cache
(Read Mode)

FIG. 4

System Address
(A20) (A21:A31)

210



Dual function D-Cache (Write Mode).
Parity Array as Trace.

F/G. 6

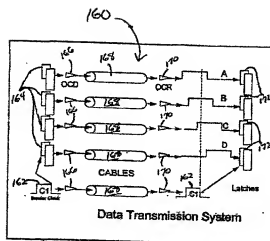


FIGURE 7

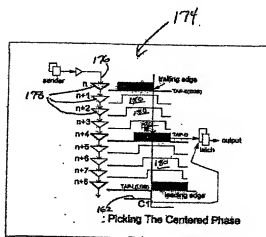


FIGURE 8

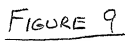


FIGURE 9

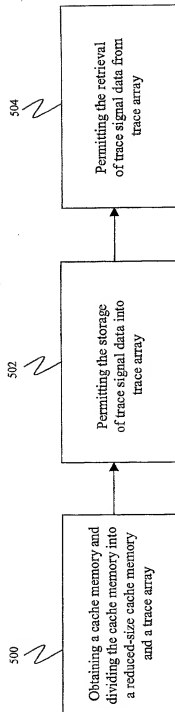


FIGURE 10